



Assembly Top Drawing

NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
3. JUMPER SHUNTS INSTALLATION: JP2- PULSE, JP3 - ON, JP6

REVISIONS		
REV	APPR	DATE

APPROVALS		LINEAR TECHNOLOGY	
DRAWN	INIT	DATE	
CHECK			
DESIGN	Antonin		
ENGR	TomG		
SCALE = NONE		DES-230730	
SHT 1 of 1			

TITLE: LTC3542EDC		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
Monolithic Synchronous Buck Regulator			
SIZE	NONE	Demo Circuit 993A	REV. 2